



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :

Dayna Byrne
Jeffrey Holm

Serial No. : 09/992,043

Group Art Unit : 2113

Filed : November 16, 2001

Examiner : Wilson, Yolanda

For : Shared Embedded Trace Macrocell

Atty Docket : 1496.00174 / 01-559

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Connie Del Castillo

12/14/05 *Connie del Castillo*

Date

Signature

SUBMISSION OF FORMAL DRAWINGS PURSUANT TO 37 C.F.R. §1.85

Official Draftsman

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant hereby substitutes the enclosed formal drawings for those presently in the above referenced application.

LSI Logic Corporation
1621 Barber Lane, MS D-106
Milipitas, CA 95035
408-433-7475

Respectfully submitted,

Sandeep Jaggi

Sandeep Jaggi

Reg. No. 43,331

Date: 12/12/05